

Remarks

Claim Rejections – 35 USC 103

Claims 1, 2 5-10 were rejected under 35 USC 103 as unpatentable over Okamura in view of Ker. It is respectfully pointed out that Okamura teaches using the use of MOSFETs as switching elements. It does not teach the use of SCRs as switching elements. Therefore, even if Ker does teach replacing an SCR with an LVTSCR, this does not teach replacing a MOSFET with an LVTSCR.

One of the important premises of the present invention includes the use of LVTSCRs operating in snapback mode to allow much larger currents to be handled and thus allow the capacitors to be charged more quickly, and to provide lower parasitic capacitance. This is discussed on page 4, lines 20 to page 5, line 5.

Thus the use of MOSFETs in Okamura simply restates the prior art discussed with respect to Figure 3 of the present application.

Since the inclusion of Okamura was presumably based on a mis-reading that it teaches the use of SCRs (which it doesn't), it is respectfully submitted that the finality of this Office Action is improper and should be withdrawn.

The examiner is asked to provide a substitute reference that teaches the use of SCR switches to charge capacitors in parallel and discharge them in series, or instead to allow the present claims as they stand.

Respectfully Submitted,

Dated: 7/21/2005



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